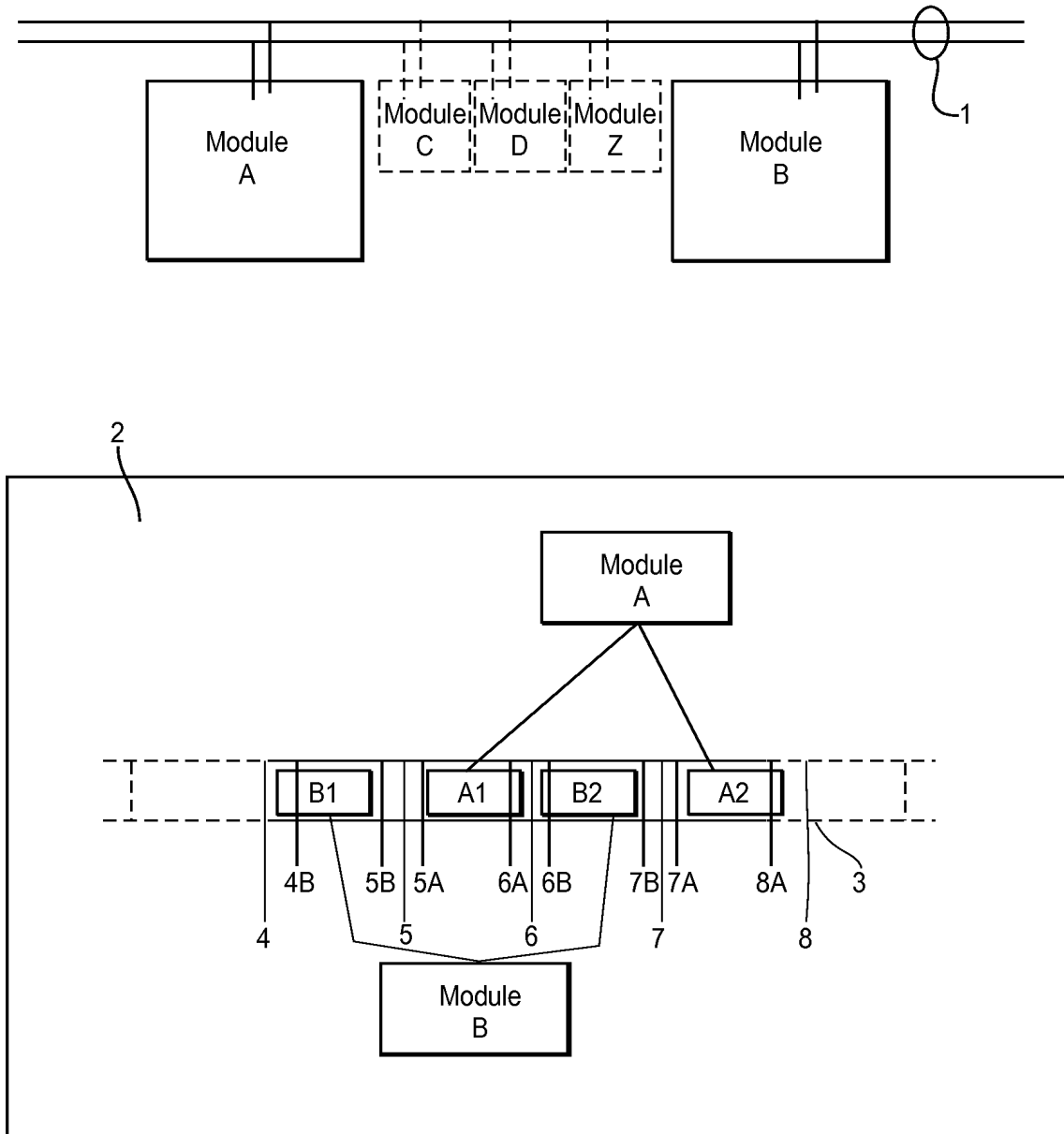
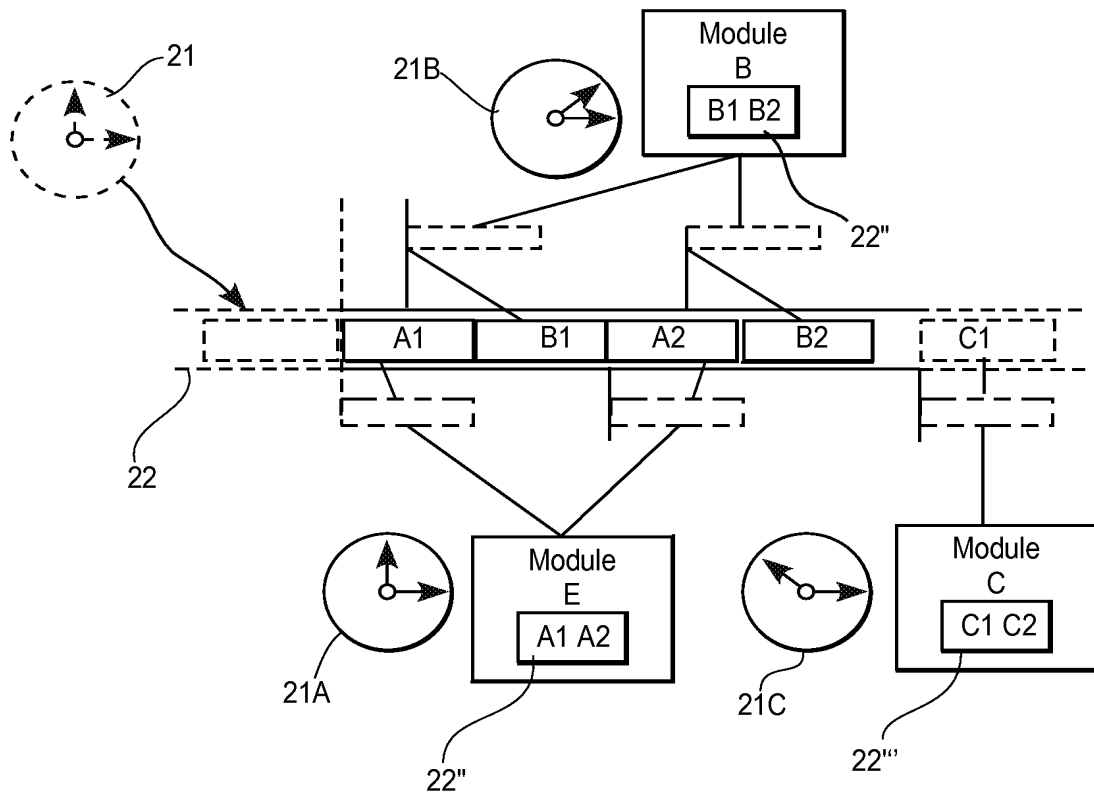


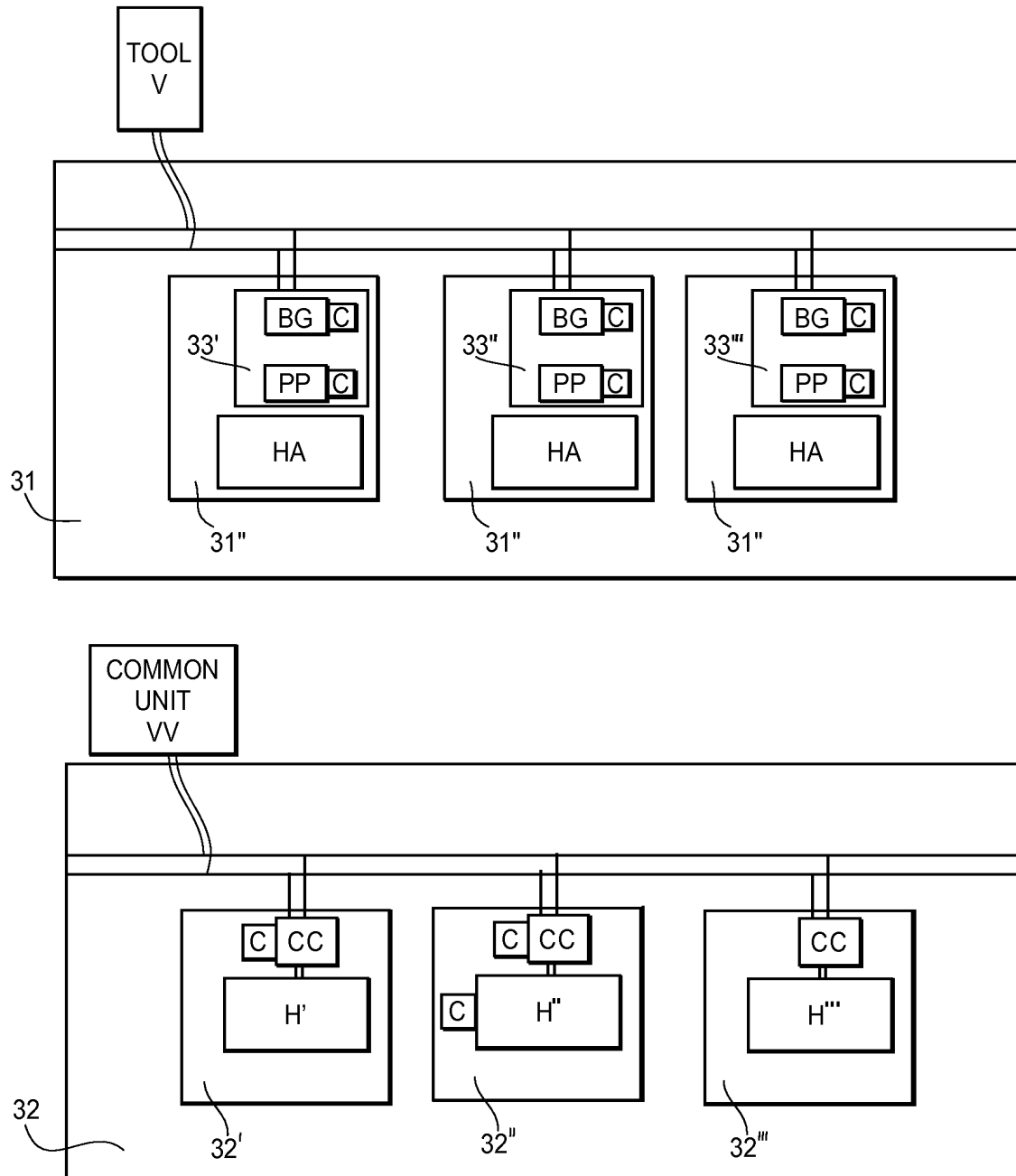
FIG. 1



LEGEND
A1, A2, B1, B2=Messages

FIG. 2**LEGEND**

A1, A2, B1, B2, C1, C2=Messages

FIG. 3**LEGEND**

BG=BUS Guardian

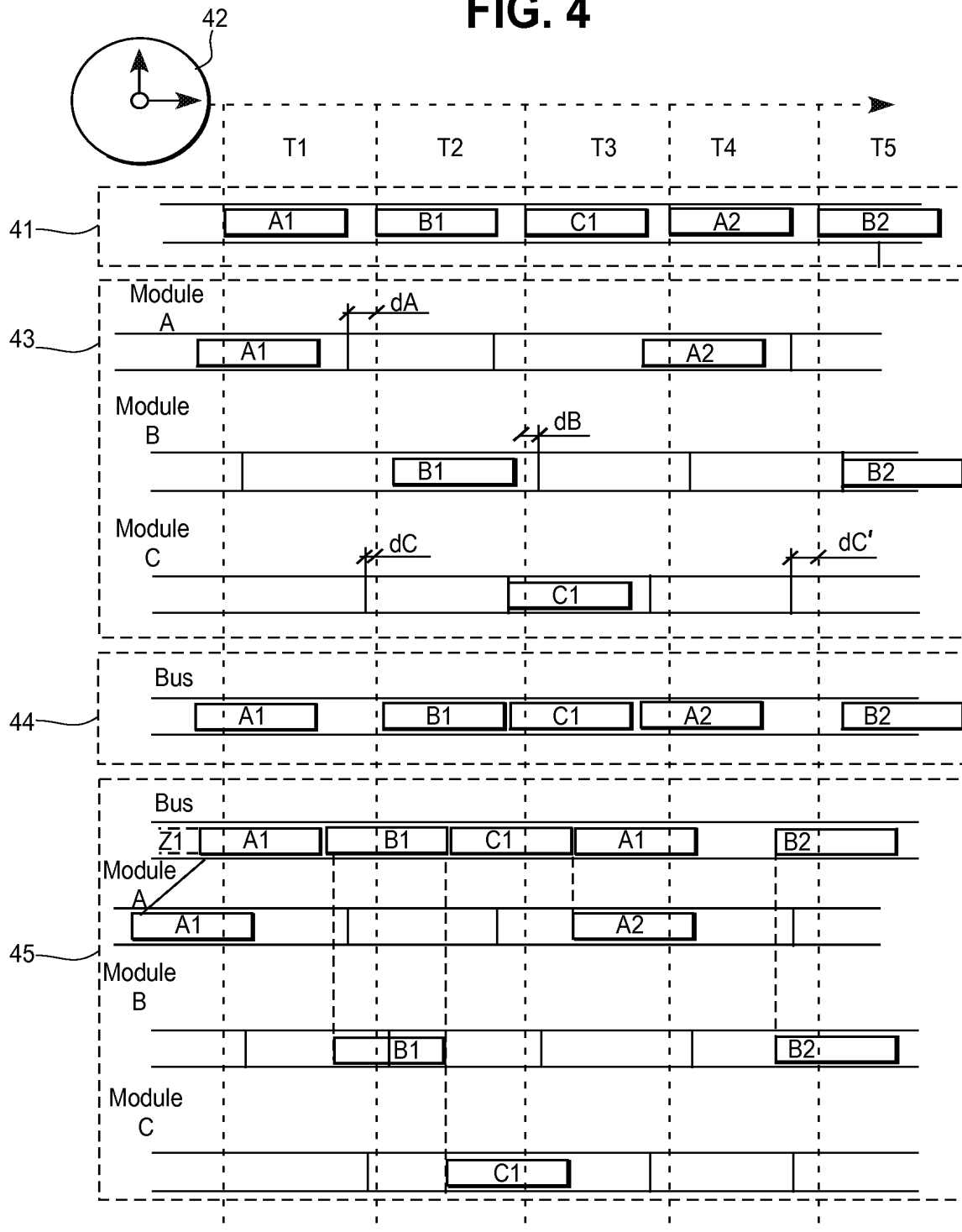
PP=Protocol Processor

HA=Requisite Hardware

C=Clock

CC=Can Controller

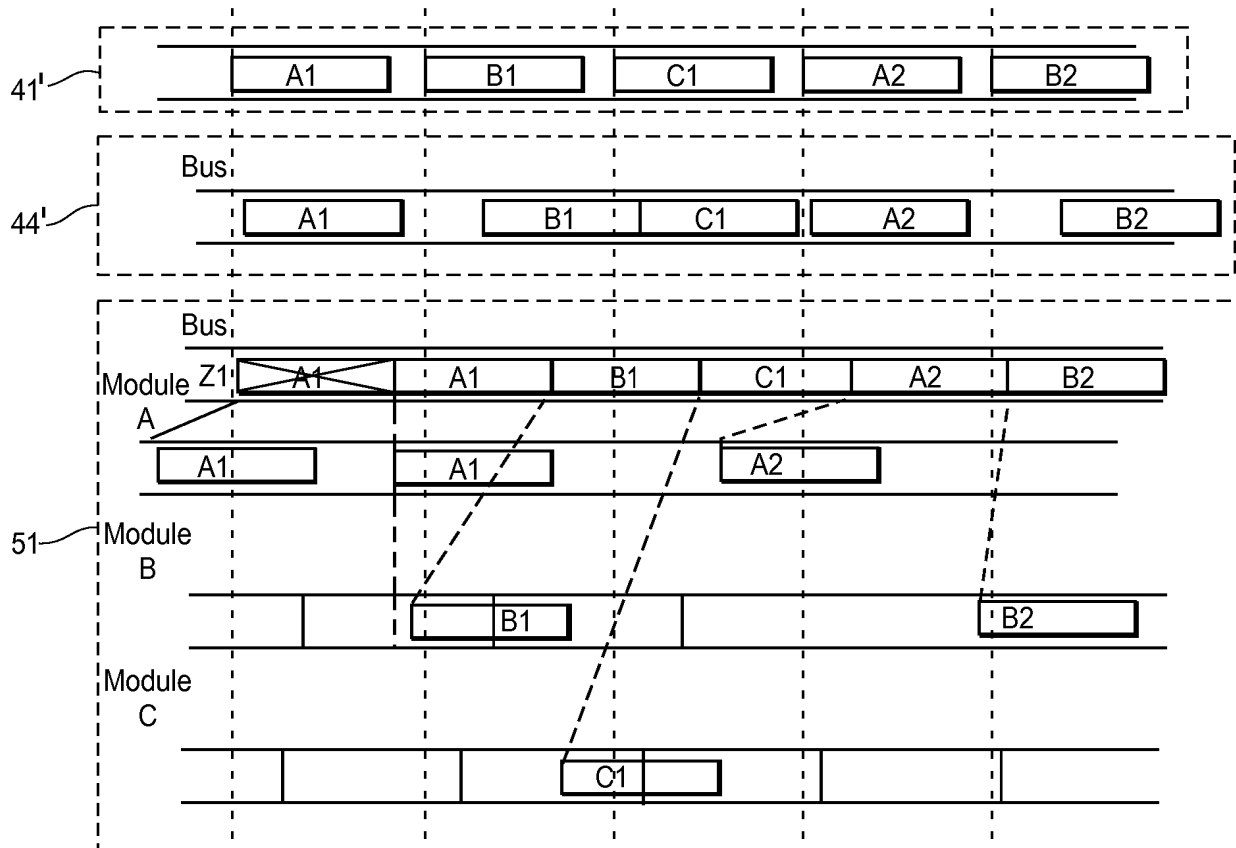
H', H'', H'''=Hardware

FIG. 4**LEGEND**

T1, T2, T3, T4, T5=Time slot

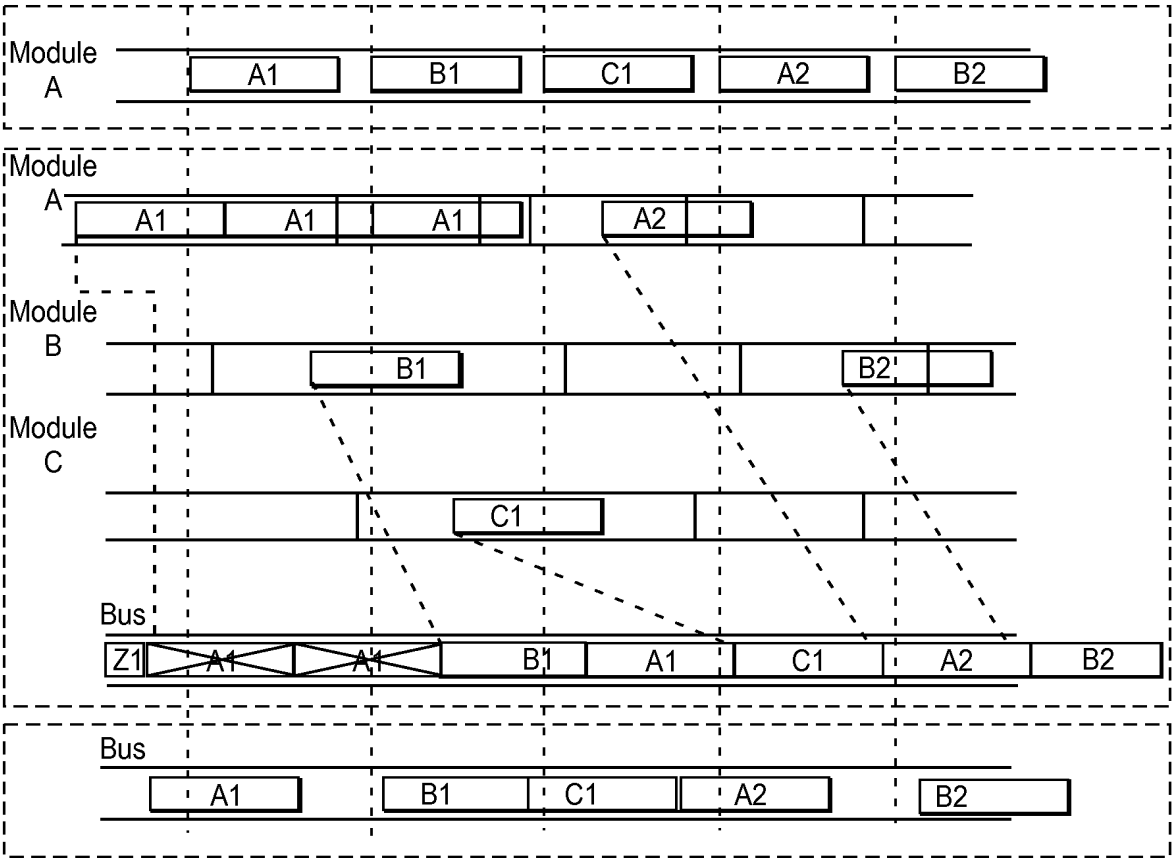
A1, A2, B1, B2, C1, Z1 =Message

 dA =Clock delta in module A dB =Clock delta in module B dC =Clock delta in module C dC' =Increased clock delta in module C

FIG. 5**LEGEND**

A1, A2, B1, B2, C1, Z1=Messages

FIG. 6



LEGEND
A1, A2, B1, C1, Z1=Messages

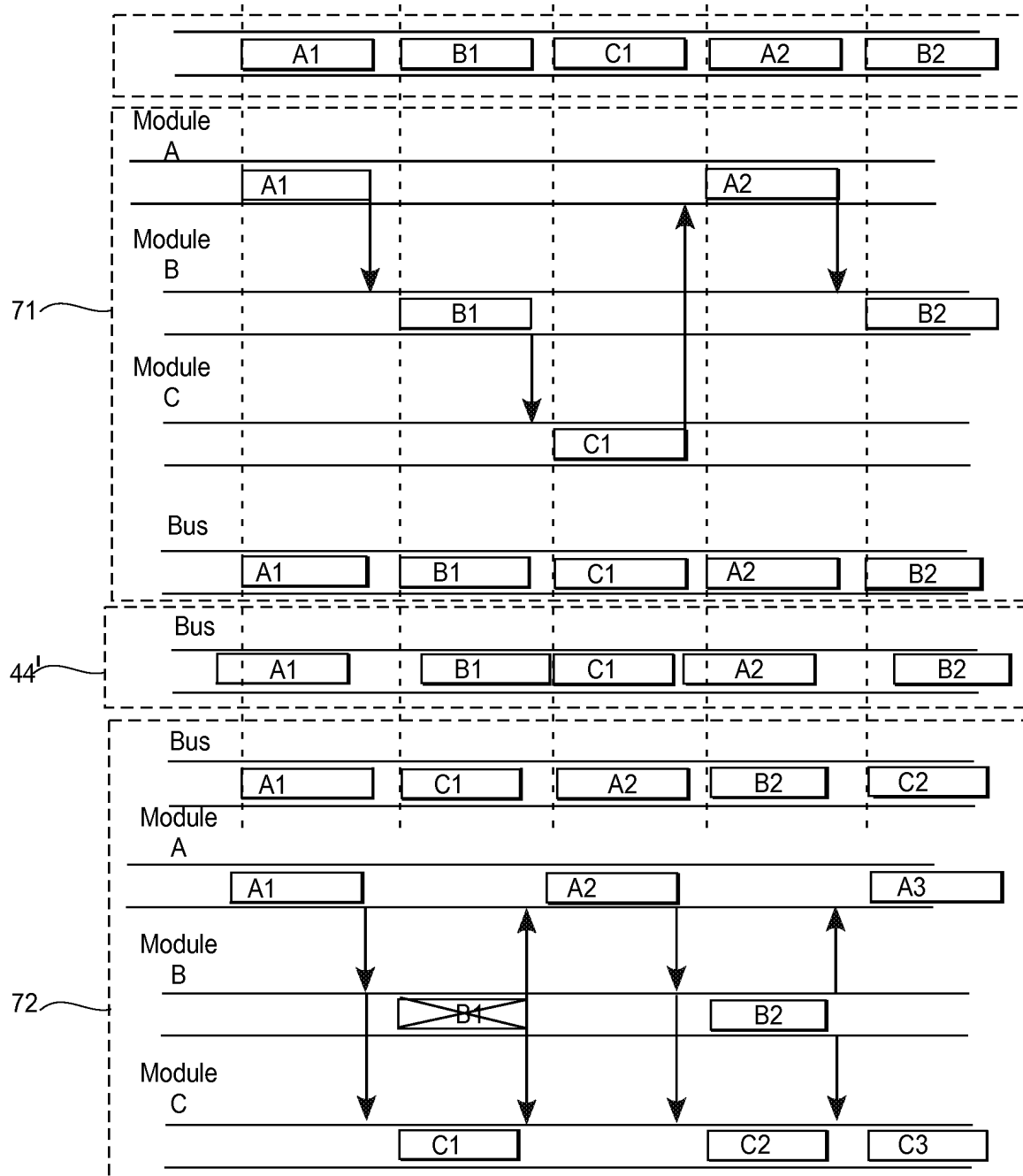
FIG. 7

FIG. 8